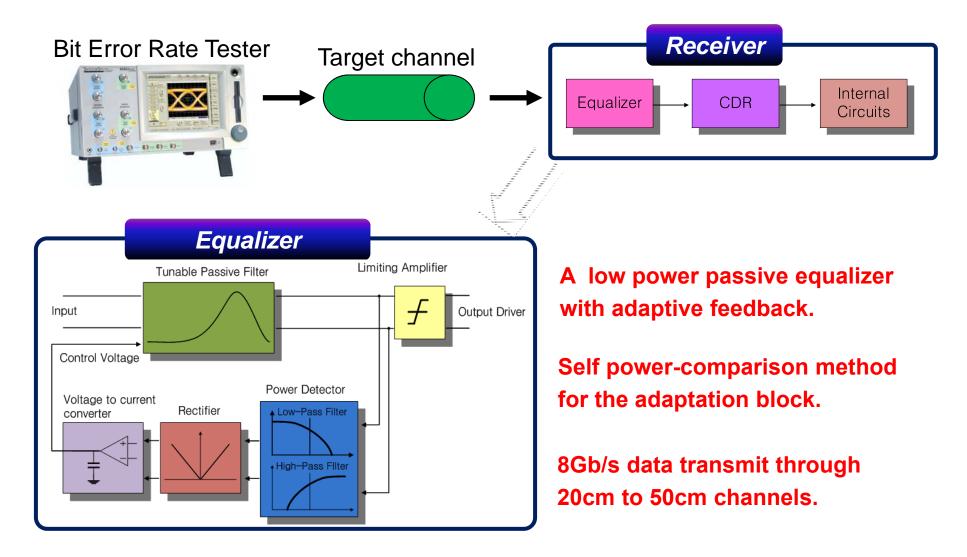
Low Power Adaptive Passive equalizer

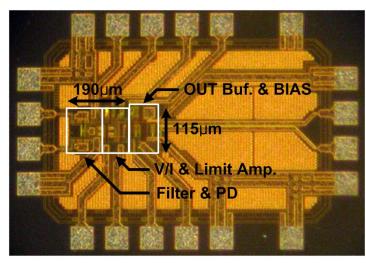
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Low Power Adaptive Passive equalizer



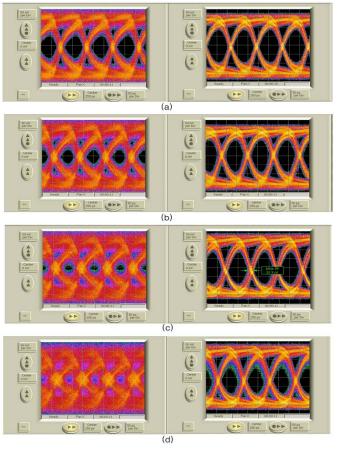
Die photographs of adaptive EQ

Achieves the error-free condition (BER < 10⁻¹³) at all target channels.

The equalizer has very small power efficiency of 0.84 mW/Gbps and occupies only 0.021 mm² of chip area in 0.18-µm CMOS technology.

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Measured waveform before (left) and after (right) equalization at 8 Gb/s of (a) 20-cm FR4 PCB trace (b) 30-cm FR4 PCB trace (c) 40-cm FR4 PCB trace (d) 50-cm FR4 PCB trace